

**Notice of References Cited**Application/Control No.  
09/592,207Applicant(s)/Patent Under  
Reexamination  
NAGARASA ET AL.Examiner  
Albert WangArt Unit  
2185

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			7
	D	US-			
	E	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Dallas Semiconductor Datasheet, "DS1020 Programmable 8-Bit Silicon Delay Line", November 17, 1999.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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